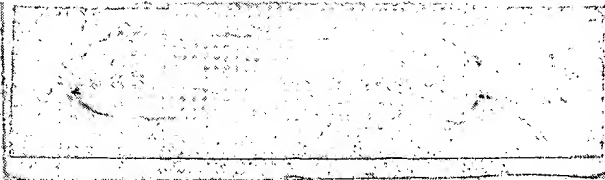


704/920

1000 U.S. PTO
09/855776
05/16/01

Class	Subclass	ISSUE CLASSIFICATION



PATENT NUMBER

U.S. UTILITY Patent Application

M/E SCANNED <i>Hm</i> O.A. <i>QAT</i>	O.I.P.E. PATENT DATE
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APPLICATION NO. 09/855776	CONT/PRIOR F	CLASS 700 712	SUBCLASS 215 214	ART UNIT 2121 2183	EXAMINER <i>Chen, Eddie</i> <i>Huisman</i>
APPLICANTS Norichika Kumamoto Toru Tsuruta Hideki Yoshizawa					
TITLE Processor control apparatus, processor, and processor controlling method					

PTO-2040
12/99

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ISSUING CLASSIFICATION										
ORIGINAL			CROSS REFERENCE(S)							
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION										

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) _____ (Date)			ISSUE FEE	
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